



7th Non-Volatile Memories Workshop

La Jolla, California USA

March 6-8, 2016

<http://nvmw.ucsd.edu>

NVMW 2016

Organizers

Paul Siegel, UCSD ECE/CMRR

Eitan Yaakobi, Technion

Steven Swanson, UCSD CSE

Program Committee

Idan Alrod, SanDisk

Remzi H. Arpaci-Dusseau, Univ. of Wisconsin

Mario Blaum, IBM

Bruce Childers, Univ. of Pittsburgh

John Davis, Pure Storage

Peter Desnoyers, Northwestern University

Alexander Driskill-Smith, Samsung

Maya Gokhale, LLNL

Kiran Gunnam, HGST

Erich Haratsch, Seagate

Andrew Jiang, Texas A&M

Edwin Kan, Cornell University

Arun Kanuparthi, Intel

Robert Mateescu, HGST Research

Mike Mesnier, Intel

Ethan Miller, UC Santa Cruz

Ravi Motwani, Intel

Narasimha Reddy, Texas A&M

Mircea Stam, Univ. of Virginia

Hemant Thapar, OmniTier Storage

Hung-Wei Tseng, UCSD

Hironori Uchikawa, Toshiba

Rick Wesel, UCLA

Gala Yadgar, Technion

Xinmiao Zhang, SanDisk

Presented By



<http://cmrr.ucsd.edu>



<http://nvsl.ucsd.edu>

The 7th Annual Non-Volatile Memories Workshop (NVMW 2016) provides a unique showcase for outstanding research on solid state, non-volatile memories. It features a "vertically integrated" program that includes presentations on devices, data encoding, systems architecture, and applications related to these exciting new data storage technologies. Last year's workshop (NVMW 2015) included 40 speakers from top universities, industrial research labs, and device manufacturers and attracted over 230 attendees. (The website for NVMW 2015 can be found at <http://nvmw.ucsd.edu/2015>.) NVMW 2016 will build on this success.

The organizing committee is soliciting presentations on any topic related to non-volatile, solid state memories, including:

- Advances in memory devices or memory cell design.
- Characterization of commercial or experimental memory devices.
- Error correction and data encoding schemes for non-volatile memories.
- Advances in non-volatile memory-based storage systems.
- Operating system and file system designs for non-volatile memories.
- Security and reliability of solid-state storage systems.
- Applications of non-volatile memories to scientific, "big data", and high-performance workloads.
- Implications of non-volatile memories for applications such as databases and NoSQL systems

The goal is to facilitate the exchange of the latest ideas, insights, and knowledge that can propel future progress. To that end, presentations may include new results or work that has already been published during the 18 months prior to the submission deadline. In lieu of printed proceedings, we will post the slides and extended abstracts of the presentations online. Presentation of new work at the workshop does not preclude future publication.

Workshop submissions should be in the form of a 2-page presentation abstract. Submissions will be evaluated on the basis of impact, novelty, and general interest.

The submission deadline is November 20, 2015, with notification of acceptance by January 26, 2016.

Further details on abstract submission, technical program, tutorials, travel, social program, and travel grants will be provided at the workshop website:

<http://nvmw.ucsd.edu>