



Non-Volatile Memories Workshop

NVMW 2017

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8th Non-Volatile Memories Workshop

University of California San Diego

Price Center- Ballroom East
La Jolla, California USA
March 12-14, 2017

<http://nvmw.ucsd.edu/2017/>

The **8th Annual Non-Volatile Memories Workshop (NVMW 2017)** provides a unique showcase for outstanding research on solid-state, non-volatile memories. It features a "vertically integrated" program that includes presentations on devices, data encoding, systems architecture, and applications related to these exciting new data storage technologies.

The workshop program features around 50 speakers from university, industry, and government research labs around the world. Detailed program information is available at <http://nvmw.ucsd.edu/2017/>.

Workshop topics will include:

- Industry perspectives: interfaces, architectures, applications
- Operating systems and runtimes for NVMs
- SSD architecture and management policies
- Database and file systems
- Data persistency
- New NVM architectures and applications
- Error modeling and error mitigation codes
- Coding for structured data
- LDPC codes for NVMs
- ECC applications and ReRAM

Keynote speakers:

- Prof. H.-S. Philip Wong, Nanoelectronics Lab, Stanford University
- Martin Fink, Chief Technical Officer, Western Digital Corporation

Early registration fee:

Postdocs & Students \$150
Standard \$300

Late registration (02/27 or later):

Postdocs & Students \$170
Standard \$400

Travel grant applications are due February 20, 2017

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